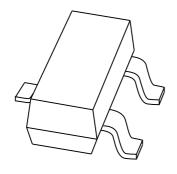
DISCRETE SEMICONDUCTORS

DATA SHEET



BC846; BC847; BC848 NPN general purpose transistors

Product specification Supersedes data of 1999 Apr 23 2002 Feb 04





NPN general purpose transistors

BC846; BC847; BC848

FEATURES

• Low current (max. 100 mA)

• Low voltage (max. 65 V).

APPLICATIONS

• General purpose switching and amplification.

DESCRIPTION

NPN transistor in a SOT23 plastic package. PNP complements: BC856, BC857 and BC858.

MARKING

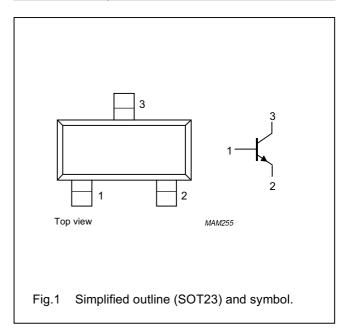
TYPE NUMBER	MARKING CODE ⁽¹⁾
BC846	1D*
BC846A	1A*
BC846B	1B*
BC847	1H*
BC847A	1E*
BC847B	1F*
BC847C	1G*
BC848B	1K*

Note

1. * = p: made in Hong Kong.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



^{* =} t: made in Malaysia.

NPN general purpose transistors

BC846; BC847; BC848

LIMITING VALUES

In accordance with the Absolute Maximum System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter			
	BC846		_	80	V
	BC847		_	50	V
	BC848		_	30	V
V _{CEO}	collector-emitter voltage	open base			
	BC846		_	65	V
	BC847		_	45	V
	BC848		_	30	V
V _{EBO}	emitter-base voltage	open collector			
	BC846; BC847		_	6	V
	BC848		_	5	V
I _C	collector current (DC)		_	100	mA
I _{CM}	peak collector current		_	200	mA
I _{BM}	peak base current		_	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	250	mW
T _{stg}	storage temperature		- 65	+150	°C
T _j	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board, standard footprint.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT	
R _{th j-a}	thermal resistance from junction to ambient	in free air; note 1	500	K/W	

Note

1. Transistor mounted on an FR4 printed-circuit board, standard footprint.

NPN general purpose transistors

BC846; BC847; BC848

CHARACTERISTICS

 T_{amb} = 25 °C; unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	V _{CB} = 30 V; I _E = 0	_	_	15	nA
		$V_{CB} = 30 \text{ V; } I_{E} = 0;$ $T_{j} = 150 ^{\circ}\text{C}$	-	-	5	μА
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0	_	_	100	nA
h _{FE}	DC current gain	$I_C = 10 \mu A; V_{CE} = 5 V$				
	BC846A; BC847A		_	90	_	
	BC846B; BC847B; BC848B		_	150	_	
	BC847C		_	270	_	
	DC current gain	I _C = 2 mA; V _{CE} = 5 V				
	BC846		110	_	450	
	BC847		110	_	800	
	BC846A; BC847A		110	180	220	
	BC846B; BC847B; BC848B		200	290	450	
	BC847C		420	520	800	
V _{CEsat}	collector-emitter saturation voltage	I _C = 10 mA; I _B = 0.5 mA	_	90	250	mV
		$I_C = 100 \text{ mA}; I_B = 5 \text{ mA};$ note 1	_	200	600	mV
V _{BEsat}	base-emitter saturation voltage	I _C = 10 mA; I _B = 0.5 mA	_	700	_	mV
		$I_C = 100 \text{ mA}; I_B = 5 \text{ mA};$ note 1	_	900	_	mV
V _{BE}	base-emitter voltage	I _C = 2 mA; V _{CE} = 5 V	580	660	700	mV
		I _C = 10 mA; V _{CE} = 5 V	_	_	770	mV
C _c	collector capacitance $V_{CB} = 10 \text{ V}; I_E = I_e = 0;$ f = 1 MHz		_	2.5	_	pF
f _T	transition frequency	vency $V_{CE} = 5 \text{ V}; I_{C} = 10 \text{ mA};$ $f = 100 \text{ MHz}$		-	_	MHz
F	noise figure $I_{C}=200~\mu\text{A; V}_{CE}$ $R_{S}=2~k\Omega;~f=1$ $B=200~Hz$		-	2	10	dB

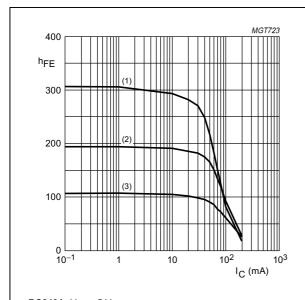
Note

1. Pulse test: $t_p \le 300 \ \mu s; \ \delta \le 0.02.$

2002 Feb 04

NPN general purpose transistors

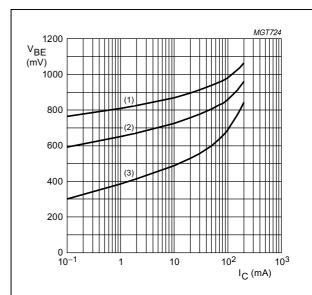
BC846; BC847; BC848



BC846A; $V_{CE} = 5 V$.

- (1) $T_{amb} = 150 \, ^{\circ}C$.
- (2) T_{amb} = 25 °C.
- (3) $T_{amb} = -55 \, ^{\circ}C$.

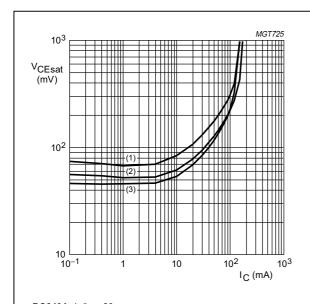
Fig.2 DC current gain as a function of collector current; typical values.



BC846A; V_{CE} = 5 V.

- (1) $T_{amb} = -55 \, ^{\circ}C$.
- (2) T_{amb} = 25 °C.
- (3) $T_{amb} = 150 \, ^{\circ}C$.

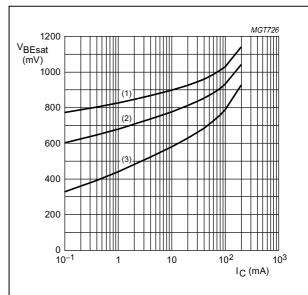
Fig.3 Base-emitter voltage as a function of collector current; typical values.



BC846A; $I_C/I_B = 20$.

- (1) T_{amb} = 150 °C.
- (2) $T_{amb} = 25 \, ^{\circ}C$.
- (3) $T_{amb} = -55 \, ^{\circ}C$.

Fig.4 Collector-emitter saturation voltage as a function of collector current; typical values.



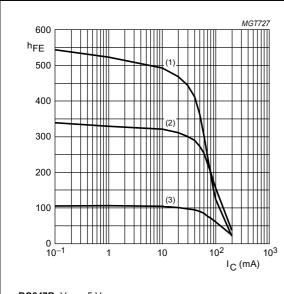
BC846A; $I_C/I_B = 10$.

- (1) $T_{amb} = -55 \, ^{\circ}C$.
- (2) T_{amb} = 25 °C.
- (3) $T_{amb} = 150 \, ^{\circ}C$.

Fig.5 Base-emitter saturation voltage as a function of collector current; typical values.

NPN general purpose transistors

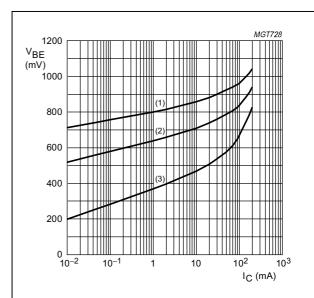
BC846; BC847; BC848



BC847B; $V_{CE} = 5 V$.

- (1) $T_{amb} = 150 \, ^{\circ}C$.
- (2) T_{amb} = 25 °C.
- (3) $T_{amb} = -55 \, ^{\circ}C$.

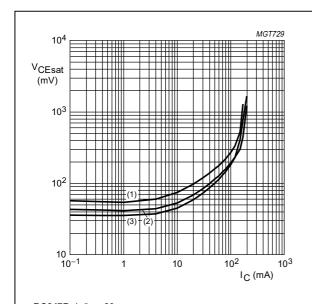
Fig.6 DC current gain as a function of collector current; typical values.



BC847B; V_{CE} = 5 V.

- (1) $T_{amb} = -55 \, ^{\circ}C$.
- (2) T_{amb} = 25 °C.
- (3) $T_{amb} = 150 \, ^{\circ}C$.

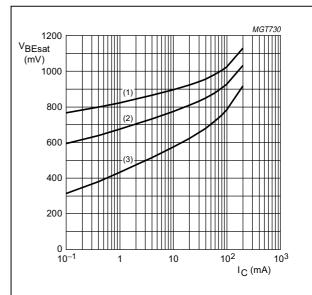
Fig.7 Base-emitter voltage as a function of collector current; typical values.



BC847B; $I_C/I_B = 20$.

- (1) T_{amb} = 150 °C.
- (2) $T_{amb} = 25 \, ^{\circ}C$.
- (3) $T_{amb} = -55 \, ^{\circ}C$.

Fig.8 Collector-emitter saturation voltage as a function of collector current; typical values.



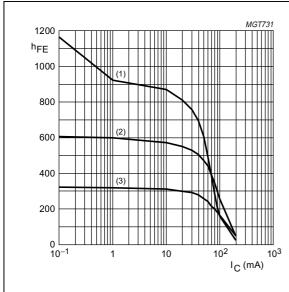
BC847B; $I_C/I_B = 10$.

- (1) $T_{amb} = -55 \, ^{\circ}C$.
- (2) T_{amb} = 25 °C.
- (3) T_{amb} = 150 °C.

Fig.9 Base-emitter saturation voltage as a function of collector current; typical values.

NPN general purpose transistors

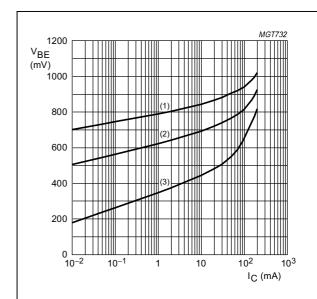
BC846; BC847; BC848



BC847C; V_{CE} = 5 V.

- (1) $T_{amb} = 150 \, ^{\circ}C$.
- (2) T_{amb} = 25 °C.
- (3) $T_{amb} = -55 \, ^{\circ}C$.

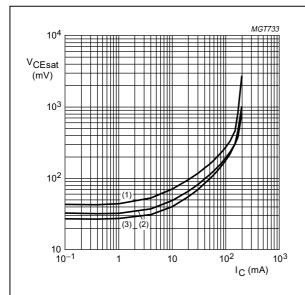
Fig.10 DC current gain as a function of collector current; typical values.



BC847C; V_{CE} = 5 V.

- (1) $T_{amb} = -55 \, ^{\circ}C$.
- (2) T_{amb} = 25 °C.
- (3) $T_{amb} = 150 \, ^{\circ}C$.

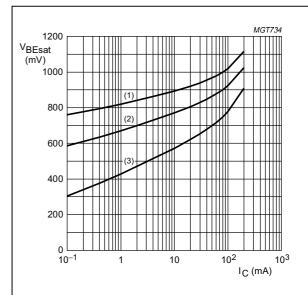
Fig.11 Base-emitter voltage as a function of collector current; typical values.



BC847C; $I_C/I_B = 20$.

- (1) T_{amb} = 150 °C.
- (2) $T_{amb} = 25 \, ^{\circ}C$.
- (3) $T_{amb} = -55 \, ^{\circ}C$.

Fig.12 Collector-emitter saturation voltage as a function of collector current; typical values.



BC847C; $I_C/I_B = 10$.

- (1) $T_{amb} = -55 \, ^{\circ}C$.
- (2) T_{amb} = 25 °C.
- (3) $T_{amb} = 150 \, ^{\circ}C$.

Fig.13 Base-emitter saturation voltage as a function of collector current; typical values.

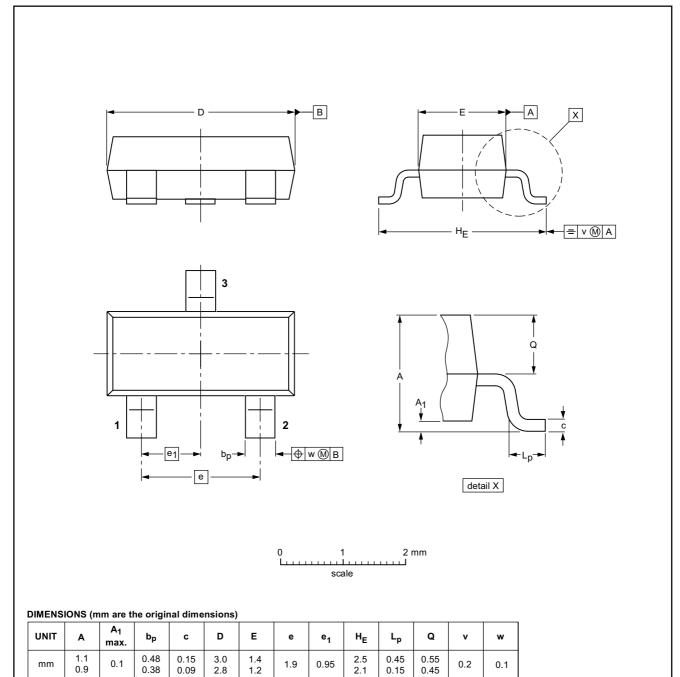
NPN general purpose transistors

BC846; BC847; BC848

PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



OUTLINE	REFERENCES		EUROPEAN	ICCUE DATE		
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT23		TO-236AB				-97-02-28- 99-09-13

2002 Feb 04 8

0.09

NPN general purpose transistors

BC846; BC847; BC848

DATA SHEET STATUS

DATA SHEET STATUS(1)	PRODUCT STATUS ⁽²⁾	DEFINITIONS
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
Product data	Production	This data sheet contains data from the product specification. Philips Semiconductors reserves the right to make changes at any time in order to improve the design, manufacturing and supply. Changes will be communicated according to the Customer Product/Process Change Notification (CPCN) procedure SNW-SQ-650A.

Notes

- 1. Please consult the most recently issued data sheet before initiating or completing a design.
- 2. The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL http://www.semiconductors.philips.com.

DEFINITIONS

Short-form specification — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information — Applications that are described herein for any of these products are for illustrative purposes only. Philips Semiconductors make no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

DISCLAIMERS

Life support applications — These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips Semiconductors customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips Semiconductors for any damages resulting from such application.

Right to make changes — Philips Semiconductors reserves the right to make changes, without notice, in the products, including circuits, standard cells, and/or software, described or contained herein in order to improve design and/or performance. Philips Semiconductors assumes no responsibility or liability for the use of any of these products, conveys no licence or title under any patent, copyright, or mask work right to these products, and makes no representations or warranties that these products are free from patent, copyright, or mask work right infringement, unless otherwise specified.

2002 Feb 04

NPN general purpose transistors

BC846; BC847; BC848

NOTES

NPN general purpose transistors

BC846; BC847; BC848

NOTES

Philips Semiconductors – a worldwide company

Contact information

For additional information please visit http://www.semiconductors.philips.com. Fax: +31 40 27 24825 For sales offices addresses send e-mail to: sales.addresses@www.semiconductors.philips.com.

© Koninklijke Philips Electronics N.V. 2002

SCA74

All rights are reserved. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner.

The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of its use. Publication thereof does not convey nor imply any license under patent- or other industrial or intellectual property rights.

Printed in The Netherlands

613514/04/pp12

Date of release: 2002 Feb 04

Document order number: 9397 750 09165

Let's make things better.

Philips Semiconductors



