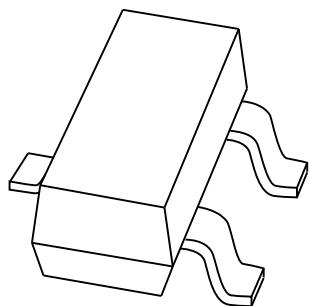


DATA SHEET



BF820; BF822 NPN high-voltage transistors

Product specification

1999 Apr 15

Supersedes data of 1997 Apr 23

NPN high-voltage transistors**BF820; BF822****FEATURES**

- Low current (max. 50 mA)
- High voltage (max. 300 V).

APPLICATIONS

- Telephony and professional communication equipment.

DESCRIPTION

NPN high-voltage transistor in a SOT23 plastic package.
PNP complements: BF821; BF823.

MARKING

TYPE NUMBER	MARKING CODE ⁽¹⁾
BF820	1V*
BF822	1X*

Note

1. * = p : Made in Hong Kong.
* = t : Made in Malaysia.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector

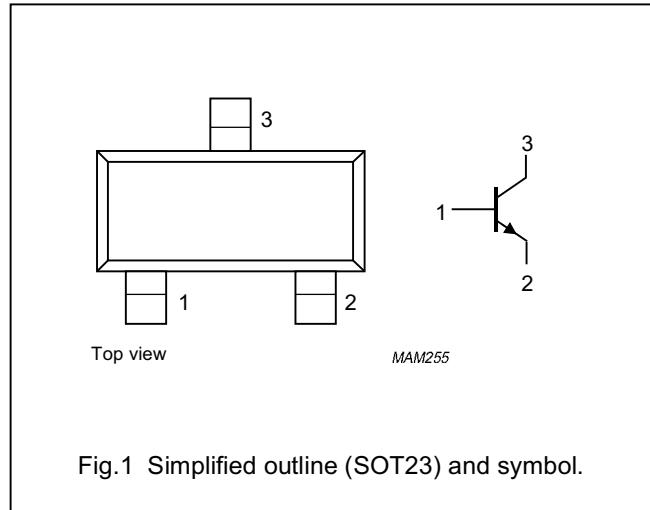


Fig.1 Simplified outline (SOT23) and symbol.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage BF820 BF822	open emitter	—	300	V
V_{CEO}	collector-emitter voltage BF820 BF822	open base	—	300	V
V_{EBO}	emitter-base voltage	open collector	—	5	V
I_C	collector current (DC)		—	50	mA
I_{CM}	peak collector current		—	100	mA
I_{BM}	peak base current		—	50	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25^\circ\text{C}$; note 1	—	250	mW
T_{stg}	storage temperature		-65	+150	°C
T_j	junction temperature		—	150	°C
T_{amb}	operating ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

NPN high-voltage transistors

BF820; BF822

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-a}$	thermal resistance from junction to ambient	note 1	500	K/W

Note

- Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS $T_j = 25^\circ\text{C}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I_{CBO}	collector cut-off current	$I_E = 0; V_{CB} = 200\text{ V}$	–	10	nA
		$I_E = 0; V_{CB} = 200\text{ V}; T_j = 150^\circ\text{C}$	–	10	μA
I_{EBO}	emitter cut-off current	$I_C = 0; V_{EB} = 5\text{ V}$	–	50	nA
h_{FE}	DC current gain	$I_C = 25\text{ mA}; V_{CE} = 20\text{ V}$	50	–	
V_{CEsat}	collector-emitter saturation voltage	$I_C = 30\text{ mA}; I_B = 5\text{ mA}$	–	600	mV
C_{re}	feedback capacitance	$I_C = i_c = 0; V_{CB} = 30\text{ V}; f = 1\text{ MHz}$	–	1.6	pF
f_T	transition frequency	$I_C = 10\text{ mA}; V_{CE} = 10\text{ V}; f = 100\text{ MHz}$	60	–	MHz

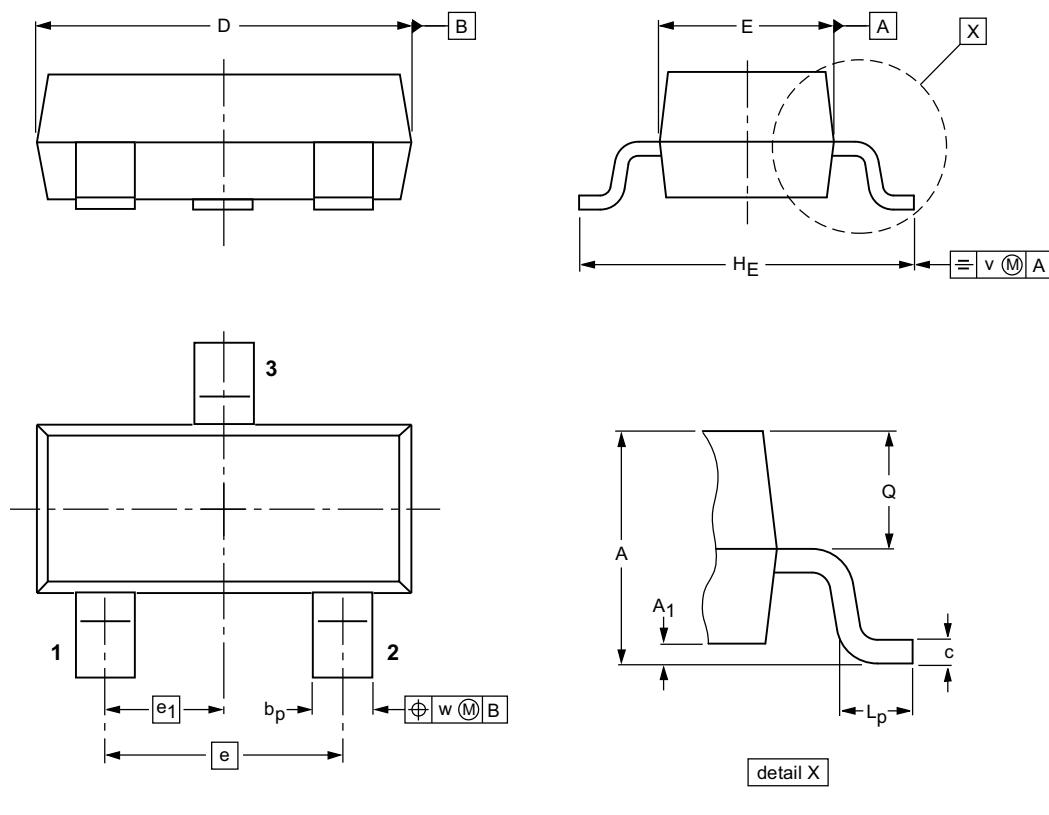
NPN high-voltage transistors

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PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



0 1 2 mm
scale

DIMENSIONS (mm are the original dimensions)

UNIT	A	A_1 max.	b_p	c	D	E	e	e_1	H_E	L_p	Q	v	w
mm	1.1 0.9	0.1	0.48 0.38	0.15 0.09	3.0 2.8	1.4 1.2	1.9	0.95	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT23						97-02-28

NPN high-voltage transistors**BF820; BF822****DEFINITIONS**

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

LIFE SUPPORT APPLICATIONS

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NPN high-voltage transistors

BF820; BF822

NOTES

NPN high-voltage transistors

BF820; BF822

NOTES

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