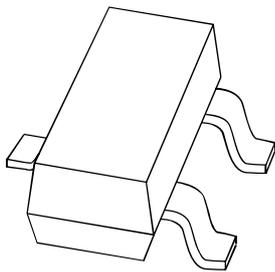


DATA SHEET



BSR13; BSR14 NPN switching transistors

Product data sheet
Supersedes data of 1999 Apr 15

2004 Jan 13

NPN switching transistors

BSR13; BSR14

FEATURES

- High current (max. 800 mA)
- Low voltage (max. 40 V).

APPLICATIONS

- Switching and linear applications.

DESCRIPTION

NPN switching transistor in a SOT23 plastic package.
 PNP complements: BSR15 and BSR16.

MARKING

TYPE NUMBER	MARKING CODE ⁽¹⁾
BSR13	U7*
BSR14	U8*

Note

- * = p : Made in Hong Kong.
 * = t : Made in Malaysia.
 * = W : Made in China.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector

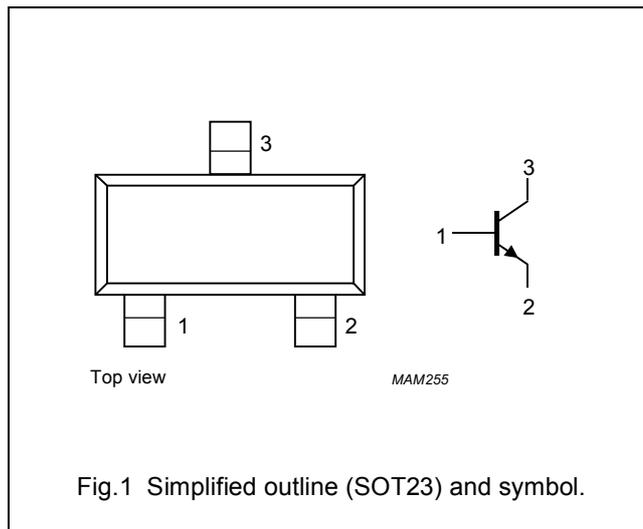


Fig.1 Simplified outline (SOT23) and symbol.

ORDERING INFORMATION

TYPE NUMBER	PACKAGE		
	NAME	DESCRIPTION	VERSION
BSR13	-	plastic surface mounted package; 3 leads	SOT23
BSR14			

NPN switching transistors

BSR13; BSR14

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter			
	BSR13		–	60	V
	BSR14		–	75	V
V _{CEO}	collector-emitter voltage	open base			
	BSR13		–	30	V
	BSR14		–	40	V
V _{EBO}	emitter-base voltage	open collector			
	BSR13		–	5	V
	BSR14		–	6	V
I _C	collector current (DC)		–	800	mA
I _{CM}	peak collector current		–	800	mA
I _{BM}	peak base current		–	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	–	250	mW
T _{stg}	storage temperature		–65	+150	°C
T _j	junction temperature		–	150	°C
T _{amb}	operating ambient temperature		–65	+150	°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICST_j = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current BSR13	I _E = 0; V _{CB} = 50 V	–	30	nA
		I _E = 0; V _{CB} = 50 V; T _j = 150 °C	–	10	μA
	collector cut-off current BSR14	I _E = 0; V _{CB} = 60 V	–	10	nA
		I _E = 0; V _{CB} = 60 V; T _j = 150 °C	–	10	μA
I _{EBO}	emitter cut-off current	I _C = 0; V _{EB} = 5 V			
	BSR13		–	30	nA
	BSR14		–	10	nA

NPN switching transistors

BSR13; BSR14

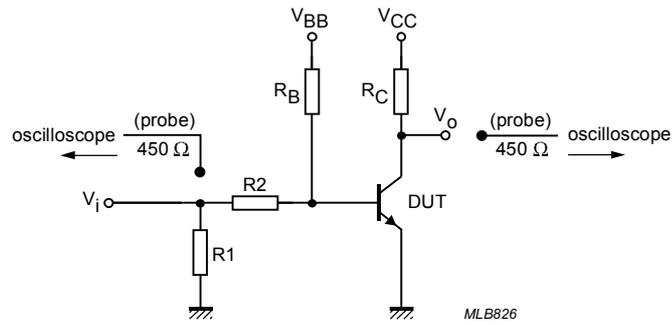
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
h_{FE}	DC current gain	$I_C = 0.1 \text{ mA}; V_{CE} = 10 \text{ V}; \text{note 1}$	35	–	
		$I_C = 1 \text{ mA}; V_{CE} = 10 \text{ V}; \text{note 1}$	50	–	
		$I_C = 10 \text{ mA}; V_{CE} = 10 \text{ V}; \text{note 1}$	75	–	
		$I_C = 150 \text{ mA}; V_{CE} = 10 \text{ V}; \text{note 1}$	100	300	
		$I_C = 150 \text{ mA}; V_{CE} = 1 \text{ V}; \text{note 1}$	50	–	
h_{FE}	DC current gain	$I_C = 500 \text{ mA}; V_{CE} = 10 \text{ V}; \text{note 1}$			
	BSR13		30	–	
V_{CEsat}	collector-emitter saturation voltage	$I_C = 150 \text{ mA}; I_B = 15 \text{ mA}$			
			BSR13	–	400
	BSR14	–	300	mV	
	collector-emitter saturation voltage	$I_C = 500 \text{ mA}; I_B = 50 \text{ mA}$			
BSR13			–	1.6	V
BSR14	–	1	V		
V_{BEsat}	base-emitter saturation voltage	$I_C = 150 \text{ mA}; I_B = 15 \text{ mA}$			
			BSR13	–	1.3
	BSR14	0.6	1.2	V	
	base-emitter saturation voltage	$I_C = 500 \text{ mA}; I_B = 50 \text{ mA}$			
BSR13			–	2.6	V
BSR14	–	2	V		
C_c	collector capacitance	$I_E = I_e = 0; V_{CB} = 10 \text{ V}; f = 1 \text{ MHz}$	–	8	pF
f_T	transition frequency	$I_C = 20 \text{ mA}; V_{CE} = 20 \text{ V}; f = 100 \text{ MHz}$			
			BSR13	250	–
BSR14	300	–	MHz		
Switching times (between 10% and 90% levels); see Fig.2					
t_{on}	turn-on time	$I_{Con} = 150 \text{ mA}; I_{Bon} = 15 \text{ mA}; I_{Boff} = -15 \text{ mA}$	–	35	ns
t_d	delay time		–	15	ns
t_r	rise time		–	20	ns
t_{off}	turn-off time		–	250	ns
t_s	storage time		–	200	ns
t_f	fall time		–	60	ns

Note

1. Pulse test: $t_p \leq 300 \mu\text{s}; \delta \leq 0.02$.

NPN switching transistors

BSR13; BSR14



$V_i = 9.5 \text{ V}$; $T = 500 \text{ } \mu\text{s}$; $t_p = 10 \text{ } \mu\text{s}$; $t_r = t_f \leq 3 \text{ ns}$.
 $R_1 = 68 \text{ } \Omega$; $R_2 = 325 \text{ } \Omega$; $R_B = 325 \text{ } \Omega$; $R_C = 160 \text{ } \Omega$.
 $V_{BB} = -3.5 \text{ V}$; $V_{CC} = 29.5 \text{ V}$.
Oscilloscope: input impedance $Z_i = \geq 100 \text{ } \Omega$.

Fig.2 Test circuit for switching times.

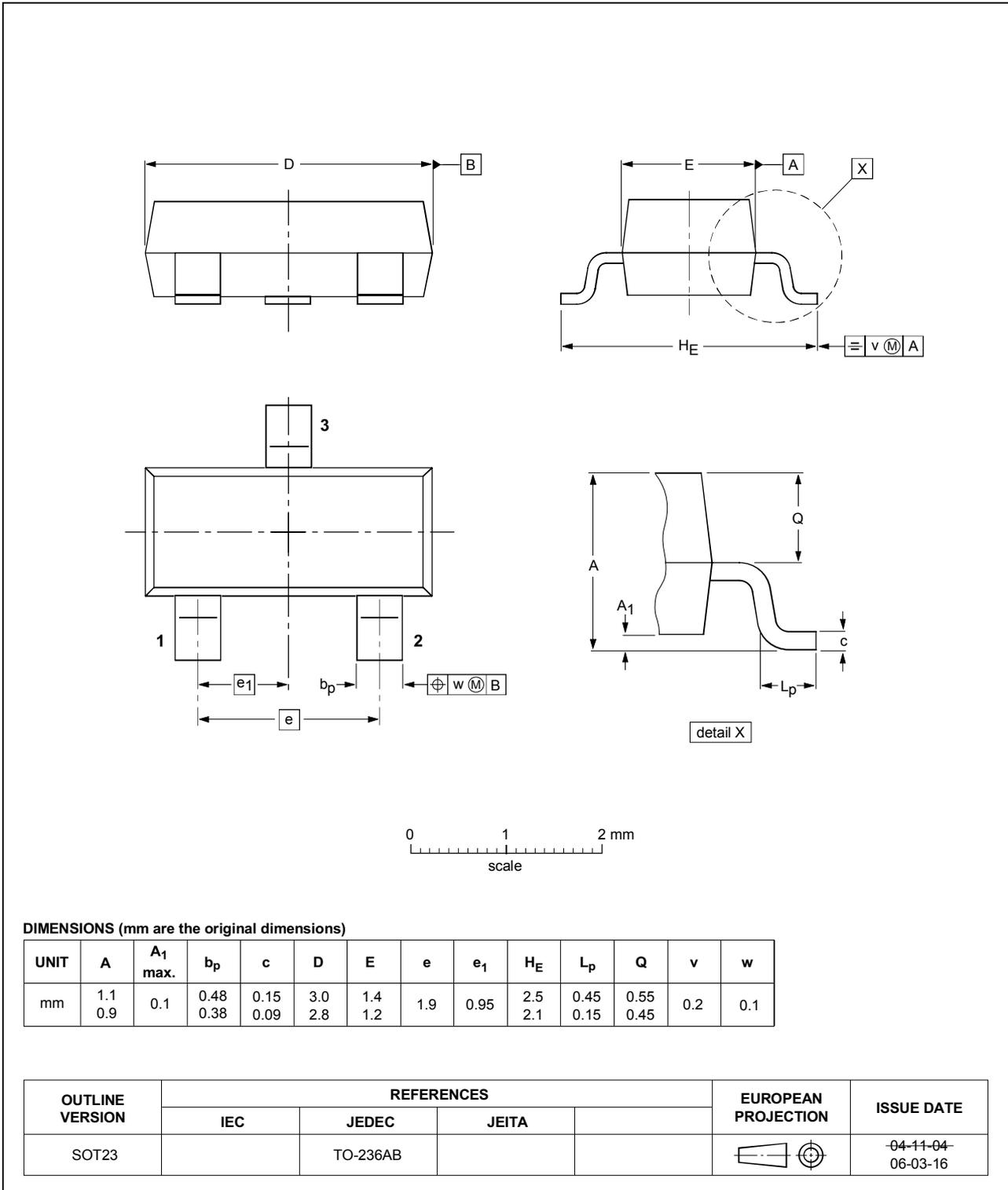
NPN switching transistors

BSR13; BSR14

PACKAGE OUTLINE

Plastic surface-mounted package; 3 leads

SOT23



NPN switching transistors

BSR13; BSR14

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

1. Please consult the most recently issued document before initiating or completing a design.
2. The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

DISCLAIMERS

General — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in medical, military, aircraft, space or life support equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) may cause permanent damage to the device. Limiting values are stress ratings only and operation of the device at these or any other conditions

above those given in the Characteristics sections of this document is not implied. Exposure to limiting values for extended periods may affect device reliability.

Terms and conditions of sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nxp.com/profile/terms>, including those pertaining to warranty, intellectual property rights infringement and limitation of liability, unless explicitly otherwise agreed to in writing by NXP Semiconductors. In case of any inconsistency or conflict between information in this document and such terms and conditions, the latter will prevail.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

NXP Semiconductors

Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

For additional information please visit: <http://www.nxp.com>

For sales offices addresses send e-mail to: salesaddresses@nxp.com

© NXP B.V. 2009

All rights are reserved. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner.

The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of its use. Publication thereof does not convey nor imply any license under patent- or other industrial or intellectual property rights.

Printed in The Netherlands

R75/04/pp8

Date of release: 2004 Jan 13

Document order number: 9397 750 12419

