High Voltage Power Transistors

DPAK For Surface Mount Applications

Designed for line operated audio output amplifier, switchmode power supply drivers and other switching applications.

- Lead Formed for Surface Mount Applications in Plastic Sleeves (No Suffix)
- Straight Lead Version in Plastic Sleeves ("-1" Suffix)
- Lead Formed Version in 16 mm Tape and Reel ("T4" Suffix)
- Electrically Similar to Popular MJE340 and MJE350
- 300 V (Min) VCEO(sus)
- 0.5 A Rated Collector Current

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	VCEO	300	Vdc
Collector–Base Voltage	V _{CB}	300	Vdc
Emitter–Base Voltage	VEB	3	Vdc
Collector Current — Continuous — Peak	IC	0.5 0.75	Adc
Total Power Dissipation @ T _C = 25°C Derate above 25°C	PD	15 0.12	Watts W/°C
Total Power Dissipation* @ T _A = 25°C Derate above 25°C	PD	1.56 0.012	Watts W/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{ heta$ JC	8.33	°C/W
Thermal Resistance, Junction to Ambient*	$R_{ heta JA}$	80	°C/W
Lead Temperature for Soldering Purpose	TL	260	°C

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit		
OFF CHARACTERISTICS						
Collector–Emitter Sustaining Voltage (1) (I _C = 1 mAdc, I _B = 0)	V _{CEO(sus)}	300	ı	Vdc		
Collector Cutoff Current (V _{CB} = 300 Vdc, I _E = 0)	ICBO	_	0.1	mAdc		
Emitter Cutoff Current ($V_{BE} = 3 \text{ Vdc}, I_{C} = 0$)	I _{EBO}		0.1	mAdc		
ON CHARACTERISTICS (1)						
DC Current Gain (I_C = 50 mAdc, V_{CE} = 10 Vdc)	h _{FE}	30	240	_		

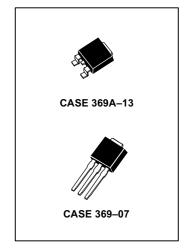
^{*} When surface mounted on minimum pad sizes recommended.

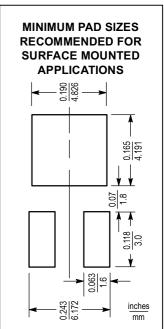
Preferred devices are Motorola recommended choices for future use and best overall value.

MJD340* PNP MJD350*

*Motorola Preferred Device

SILICON
POWER TRANSISTORS
0.5 AMPERE
300 VOLTS
15 WATTS







⁽¹⁾ Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.

TYPICAL CHARACTERISTICS

MJD340

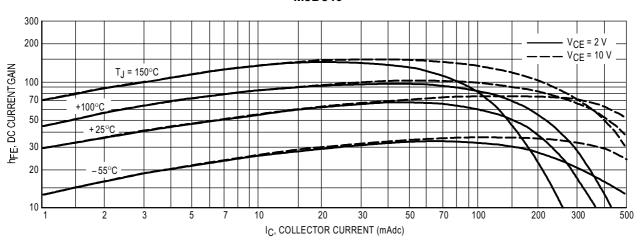


Figure 1. DC Current Gain

MJD340

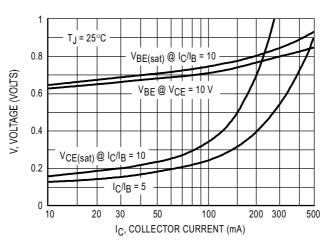
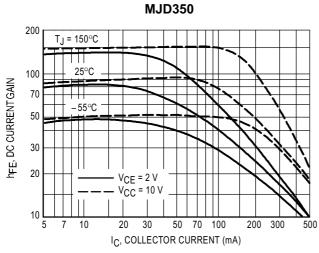


Figure 2. "On" Voltages



0.8

VBE(sat) @ IC/IB = 10

VBE @ VCE = 10 V

0.2

VCE(sat)

IC/IB = 10

IC/IB = 10

IC/IB = 5

IC, COLLECTOR CURRENT (mA)

MJD350

Figure 3. DC Current Gain

Figure 4. "On" Voltages

MJD340 MJD350

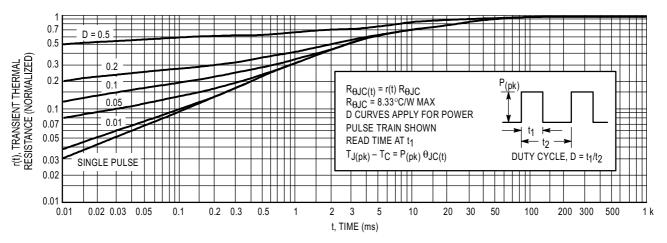


Figure 5. Thermal Response

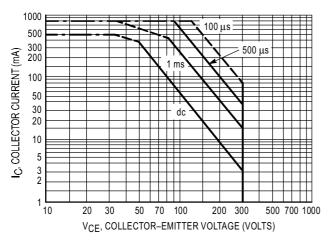


Figure 6. Active Region Safe Operating Area

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate $I_{\text{C}} - V_{\text{CE}}$ limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 6 is based on $T_{J(pk)} = 150^{\circ}C$; T_{C} is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)} \le 150^{\circ}C$. $T_{J(pk)}$ may be calculated from the data in Figure 5. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

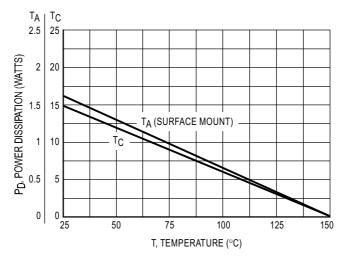
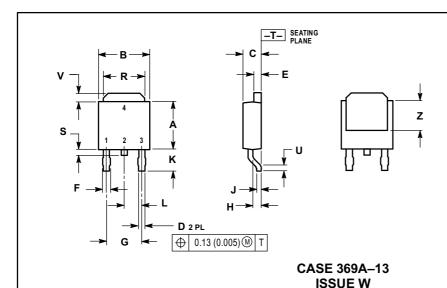


Figure 7. Power Derating

PACKAGE DIMENSIONS



- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.235	0.250	5.97	6.35
В	0.250	0.265	6.35	6.73
С	0.086	0.094	2.19	2.38
D	0.027	0.035	0.69	0.88
Е	0.033	0.040	0.84	1.01
F	0.037	0.047	0.94	1.19
G	0.180 BSC		4.58 BSC	
Н	0.034	0.040	0.87	1.01
J	0.018	0.023	0.46	0.58
K	0.102	0.114	2.60	2.89
L	0.090 BSC		2.29 BSC	
R	0.175	0.215	4.45	5.46
S	0.020	0.050	0.51	1.27
U	0.020		0.51	
٧	0.030	0.050	0.77	1.27
7	0.138		3 51	

STYLE 1

PIN 1. BASE COLLECTOR

- COLLECTOR

C В Е _T_ SEATING PLANE D 3 PI ⊕ 0.13 (0.005) M T **CASE 369-07 ISSUE K**

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M. 1982
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G	0.090 BSC		2.29 BSC	
Н	0.034	0.040	0.87	1.01
J	0.018	0.023	0.46	0.58
K	0.350	0.380	8.89	9.65
R	0.175	0.215	4.45	5.46
S	0.050	0.090	1.27	2.28
٧	0.030	0.050	0.77	1.27

STYLE 1:

PIN 1. BASE

COLLECTOR

EMITTER COLLECTOR

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