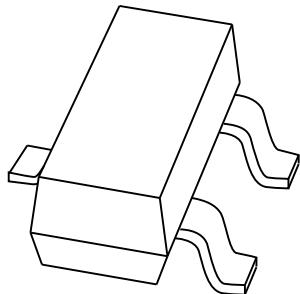


# **DATA SHEET**



## **PMBTA42; PMBTA43** **NPN high-voltage transistors**

Product specification

1997 Jul 02

Supersedes data of September 1994

File under Discrete Semiconductors, SC04

**NPN high-voltage transistors****PMBTA42; PMBTA43****FEATURES**

- Low current (max. 100 mA)
- High voltage (max. 300 V).

**APPLICATIONS**

- Telephony and professional communication equipment.

**DESCRIPTION**

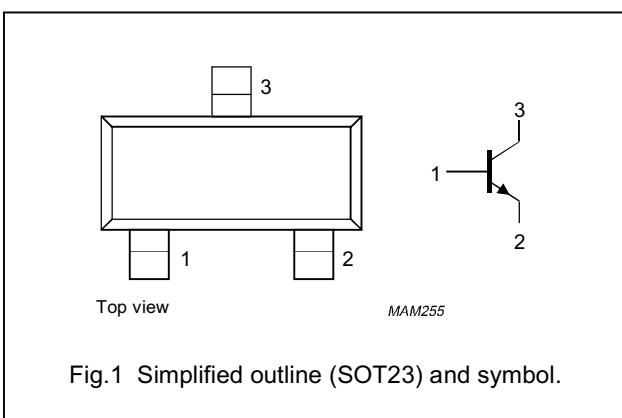
NPN high-voltage transistor in a SOT23 plastic package.  
PNP complements: PMBTA92 and PMBTA93.

**MARKING**

TYPE NUMBER	MARKING CODE
PMBTA42	p1D
PMBTA43	p1E

**PINNING**

PIN	DESCRIPTION
1	base
2	emitter
3	collector

**QUICK REFERENCE DATA**

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{CBO}$	collector-base voltage PMBTA42 PMBTA43	open emitter	– –	300 200	V V
$V_{CEO}$	collector-emitter voltage PMBTA42 PMBTA43	open base	– –	300 200	V V
$I_{CM}$	peak collector current		–	200	mA
$P_{tot}$	total power dissipation	$T_{amb} \leq 25^\circ\text{C}$	–	250	mW
$h_{FE}$	DC current gain	$I_C = 10 \text{ mA}; V_{CE} = 10 \text{ V}$	40	–	
$C_{re}$	feedback capacitance PMBTA42 PMBTA43	$I_C = i_c = 0; V_{CB} = 20 \text{ V}; f = 1 \text{ MHz}$	– –	3 4	pF pF
$f_T$	transition frequency	$I_C = 10 \text{ mA}; V_{CE} = 20 \text{ V}; f = 100 \text{ MHz}$	50	–	MHz

## NPN high-voltage transistors

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**LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{CBO}$	collector-base voltage PMBTA42 PMBTA43	open emitter	—	300	V
			—	200	V
$V_{CEO}$	collector-emitter voltage PMBTA42 PMBTA43	open base	—	300	V
			—	200	V
$V_{EBO}$	emitter-base voltage	open collector	—	6	V
$I_C$	collector current (DC)		—	100	mA
$I_{CM}$	peak collector current		—	200	mA
$I_{BM}$	peak base current		—	100	mA
$P_{tot}$	total power dissipation	$T_{amb} \leq 25^\circ\text{C}$ ; note 1	—	250	mW
$T_{stg}$	storage temperature		-65	+150	°C
$T_j$	junction temperature		—	150	°C
$T_{amb}$	operating ambient temperature		-65	+150	°C

**Note**

- Transistor mounted on an FR4 printed-circuit board.

**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-a}$	thermal resistance from junction to ambient	note 1	500	K/W

**Note**

- Transistor mounted on an FR4 printed-circuit board.

## NPN high-voltage transistors

PMBTA42; PMBTA43

**CHARACTERISTICS** $T_{amb} = 25^\circ\text{C}$  unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$I_{CBO}$	collector cut-off current PMBTA42	$I_E = 0; V_{CB} = 200 \text{ V}$	—	100	nA
	PMBTA43	$I_E = 0; V_{CB} = 160 \text{ V}$	—	100	nA
$I_{EBO}$	emitter cut-off current PMBTA42	$I_C = 0; V_{EB} = 6 \text{ V}$	—	100	nA
	PMBTA43	$I_C = 0; V_{EB} = 4 \text{ V}$	—	100	nA
$h_{FE}$	DC current gain	$V_{CE} = 10 \text{ V}$ $I_C = 1 \text{ mA}$ $I_C = 10 \text{ mA}$ $I_C = 30 \text{ mA}$	25 40 40	— — —	
	$V_{CEsat}$	$I_C = 20 \text{ mA}; I_B = 2 \text{ mA}$	—	500	mV
	$V_{BEsat}$	$I_C = 20 \text{ mA}; I_B = 2 \text{ mA}$	—	900	mV
	$C_{re}$	$I_C = i_c = 0; V_{CB} = 20 \text{ V}; f = 1 \text{ MHz}$	— —	3 4	pF pF
$f_T$	transition frequency	$I_C = 10 \text{ mA}; V_{CE} = 20 \text{ V}; f = 100 \text{ MHz}$	50	—	MHz

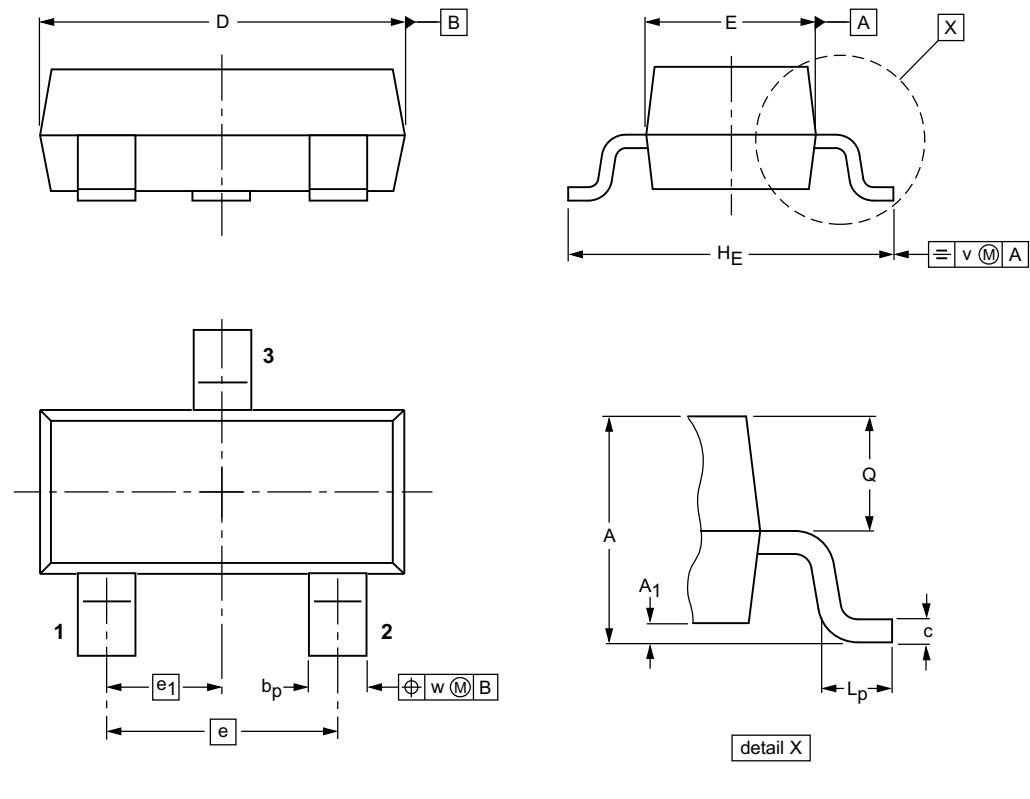
## NPN high-voltage transistors

PMBTA42; PMBTA43

## PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



0      1      2 mm  
scale

## DIMENSIONS (mm are the original dimensions)

UNIT	A	A <sub>1</sub> max.	b <sub>p</sub>	c	D	E	e	e <sub>1</sub>	H <sub>E</sub>	L <sub>p</sub>	Q	v	w
mm	1.1 0.9	0.1	0.48 0.38	0.15 0.09	3.0 2.8	1.4 1.2	1.9	0.95	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT23						97-02-28

**NPN high-voltage transistors****PMBTA42; PMBTA43**

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**DEFINITIONS**

<b>Data sheet status</b>	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
<b>Limiting values</b>	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
<b>Application information</b>	
Where application information is given, it is advisory and does not form part of the specification.	

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NPN high-voltage transistors

PMBTA42; PMBTA43

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